

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Jean-Yves Simon.	§	Group Art Unit:	2133
		§		
		§		
Serial No.:	10/764,670	§	Examiner:	Alphonse, F.
		§		
Filed:	January 26, 2004	§	Atty. Docket No.:	1962-09800
		§		
For:	Method for Streamlining Error Correction Code Computation While Reading or Programming A NAND Flash Memory	§	Client Ref. No.:	TI-36989
		§		
		§	Confirmation No.:	9476

PRELIMINARY AMENDMENT

Mail Stop RCE
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Date: December 17, 2007

INTRODUCTORY COMMENTS

Sir:

In response to the Final Office Action of October 19, 2007, Applicant presents this Preliminary Amendment and concurrently filed Request for Continued Examination. The Examiner is requested to enter the following amendments and consider the accompanying remarks. Reconsideration is respectfully requested.

Amendments to the Claims are reflected in the listings of claims which begin on page 2 of this paper.

Remarks begin on page 6 of this paper.